

Processor Settings Model LS7500v2

Crossover			Frequency		Slope			
LF w/o subwo	65Hz		24dB (24dB Oct. Butterworth				
LF w/subwoo	80Hz		24dB (24dB Oct. Butterworth				
LF - LPF			1,500Hz		24dB (24dB Oct. Butterworth		
HF - HPF			1,500Hz		24dB (24dB Oct. Butterworth		
Equalization _{Free}		Frequ	equency		Q	Level	Equalization Settings were developed in	
LF		270Hz		BW .333	4.32	-3dB	an anechoic environment with (8) units	
LF		454Hz		.5	2.87	-4dB		
LF		809Hz		.333	4.32	-4dB		
LF		2,000Hz		.05	28.9	-20dB		
HF	HF		2,200Hz		4.32	-4dB		
HF	HF 3,		3,850Hz		11.54	-4dB		
HF 8,		8,000ł	8,000Hz		2.04	+4.5dB		
HF 16		16,000	16,000Hz		2.87	+4dB		
Delay _{Time}							hange the propagation	
LF	none		positive			delay for each output depending on how much processing is on that channel		
HF	none		positive		processing			
			1					
See Application Note								
Limiting RMS Voltage See Application Note "Setting System Limiters"								
LF 45 Volts, 16 msec attack, 256 msec release, 100:1 ratio (recommended predictive peak stop @ 89 Volts or amp clipping)								
HF 15.6 Volts, 30 msec attack, 480 msec release, 100:1 ratio (recommended predictive peak stop @ 32 Volts or amp clipping)								
With Ribbon TPAC installed —————NO RMS LIMITING REQUIRED (Transparent Protection Audio Circuit)								
(for very high SPL applications, a predictive peak stop limiter @ 32 Volts is recommended)								
Gain			imes amplifiers					
LF			have equal voltage gain			*	BW Disclaimer	
HF	-2dB						Different DSP processor manufactures are not consistent	
	200						in their implementation of digital parametric EQs. The	
							SLS recommended filters will not be replicated by all	
							DSP devices. If the DSP device that is used continuously	

varies the Q value of the filter depending on the +/- dB level, the DSP will not match our settings. (Most of these

devices do not allow filter Q to be shown at all.)